

IN THE CLAIMS

This is a complete and current listing of the claims, marked with status identifiers in parentheses. The following listing of claims will replace all prior versions and listings of claims in the application.

1. (Currently Amended) A communication processor apparatus for communication in a network, comprising: having

— a processor ~~to device (10)~~ for processing incoming signals and for to at least one of ~~production produce~~ and/or provision of outgoing signals,; and

— a code memory, integrated in the processor, device (11) ~~for to provision of a code for the processor device (10),~~ characterized in that

~~the code memory device (11) is integrated in the processor device (10),~~

~~the code is being~~ in an encrypted form in the code memory, and device (11), and

~~the processor device (10) can be connected being connectable~~ to an external decoder device (12) for to decryption of at least a part of the code.

2. (Currently Amended) The communication processor apparatus as claimed in claim 1, ~~in which~~ wherein the processor ~~device (10)~~ and the code memory ~~device (11)~~ are formed by a common integrated circuit.

3. (Currently Amended) The communication processor apparatus as claimed in claim ~~12~~ or 2, ~~in which~~ wherein the common circuit is an ASIC.

4. (Currently Amended) The communication processor apparatus as claimed in ~~one of the preceding claims~~ claim 1, in

~~which~~wherein decryption information, ~~(INV) which can be made~~
available to the ~~decoding device~~decoder, ~~(12)~~ is also stored
in the code memory ~~device (11)~~.

5. (Currently Amended) The communication processor apparatus
as claimed in ~~one of the preceding claims~~claim 1, in
~~which~~wherein the code memory ~~device (11) has~~includes an input
device for inputting an encrypted code.

6. (Currently Amended) The communication processor apparatus
as claimed in claim 1, further comprising~~one of the preceding~~
~~claims, which has~~ an interchanging device ~~(24) for to~~
interchanging-interchange at least two digits in the multiple
digit code for decryption.

7. (Currently Amended) An actuator sensor interface ~~having~~
including a communication processor apparatus as claimed in
~~one of the preceding claims~~claim 1.

8. (Currently Amended) A method for communication in a
network, comprising~~the following steps~~:
—— provisioning of a code in an encrypted form in a
communication processor apparatus; and
—— ~~comparison~~comparing of data with at least one of the
code and/~~or~~ transmission of the code into the network~~;~~ and
~~characterized in that~~
—— ~~the code is provided in an encrypted form in a~~
~~communication processor apparatus,~~
- decrypting at least a part of the encrypted code ~~is~~
~~decrypted~~ outside the communication processor apparatus, ~~and~~
—— the decrypted code ~~is being~~ made available to the
communication processor apparatus.

9. (Currently Amended) The method as claimed in claim 8, ~~in which~~wherein decryption information ~~(INV)~~ is also stored together with the encrypted code in the communication processor apparatus, and is made available for decryption.

10. (Currently Amended) The method as claimed in claim 8 ~~or 9, in which~~wherein the encrypted code is generated externally and is input to the communication processor apparatus.

11. (Currently Amended) The method as claimed in ~~one of claims 8 to 10, in which~~wherein the code is a multiple digit code and at least two digits are interchanged in the communication processor apparatus for decryption.

12. (Currently Amended) The method as claimed in ~~one of claims 8 to 11, in which~~wherein the communication takes place in an AS-i network.

13. (New) A communication processor apparatus for communication in a network, comprising:

processor means for processing incoming signals and for at least one of producing and provisioning outgoing signals; and

code memory means, integrated in the processor device, for provisioning of a code for the processor device, the code being in an encrypted form in the code memory means, and the processor means being connectable to an external decoder for decryption of at least a part of the code.

14. (New) The communication processor apparatus as claimed in claim 13, wherein the processor means and the code memory means are formed by a common integrated circuit.

15. (New) The communication processor apparatus as claimed in claim 14, wherein the common circuit is an ASIC.

16. (New) The communication processor apparatus as claimed in claim 13, wherein decryption information, available to the decoder, is also stored in the code memory means.

17. (New) The communication processor apparatus as claimed in claim 13, wherein the code memory means includes an input means for inputting an encrypted code.

18. (New) The communication processor apparatus as claimed in claim 13, further comprising an interchanging means for interchanging at least two digits in the multiple digit code for decryption.

19. (New) An actuator sensor interface including a communication processor apparatus as claimed in claim 13.

20. (New) The method as claimed in claim 9, wherein the encrypted code is generated externally and is input to the communication processor apparatus.